



ELECTRONICS

Product Information

Customer :

ISSUED DATE : Aug. 25, 2008

SAMSUNG TFT-LCD

MODEL : LTA520HB09

The Information Described in this Specification is Preliminary and can be changed without prior notice

NOTE :

LCD Business

Samsung Electronics Co . , LTD.

MODEL

LTA520HB09

Doc. No

06-000-G-080825

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General Description

Description

LTA520HB09 is a color active matrix liquid crystal display (LCD) that uses amorphous silicon TFT (Thin Film Transistor) as switching components. This model is composed of a TFT LCD panel, a driver circuit and a back light unit.

The resolution of a 52.0" is 1920 x 1080 and this model can display up to 16.7 Million colors with wide viewing angle of 89° or higher in all directions. This panel is intended to support applications to provide an excellent performance for Flat Panel Display such as Home-alone Multimedia TFT-LCD TV and High Definition TV.

Features

- RoHS compliance (Pb-free)
- High contrast & aperture ratio
- SPVA (Super Patterned Vertical Align) mode
- Wide viewing angle ($\pm 178^\circ$)
- High speed response
- FHD resolution (16:9)
- Low Power consumption
- Direct Type 20CCFLs (Cold Cathode Fluorescent Lamp)
- DE (Data Enable) mode
- LVDS (Low Voltage Differential Signaling) interface

General Information

| Items | Specification | Unit | Note |
|---------------------|--|-------------------|--------|
| Module Size | 1226(H _{TYP}) x 719.2(V _{TYP}) | mm | ±1.0mm |
| | 60.0(D _{MAX}) | | |
| Weight | 18,000 (Max.) | g | |
| Pixel Pitch | 0.6(H) x 0.6(W) | mm | |
| Active Display Area | 1152.0(H) X 648.0(V) | mm | |
| Surface Treatment | Haze 14% , Anti-Glare(3H) | - | |
| Display Colors | 8bit – 16.7M | Colors | |
| Number of Pixels | 1920 x 1080 | Pixel | |
| Pixel Arrangement | RGB vertical stripe | - | |
| Display Mode | Normally Black | - | |
| Luminance of White | 500 (Typ.) | cd/m ² | |

1. Absolute Maximum Ratings

If the condition exceeds maximum ratings, it can cause malfunction or unrecoverable damage to the device.

| Item | | Symbol | Min. | Max. | Unit | Note |
|---------------------------------------|---------------|------------|---------|------|------|---------|
| Power Supply Voltage | | V_{DD} | GND-0.3 | 13 | V | (1) |
| Storage temperature | | T_{STG} | -20 | 60 | °C | (2) |
| Glass surface temperature (Operation) | Center | T_{OPR} | 0 | 50 | °C | (2),(5) |
| | T. Uniformity | ΔT | - | 10 | °C | |
| Shock (non - operating) | | S_{nop} | - | 30 | G | (3) |
| Vibration (non - operating) | | V_{nop} | - | 1.5 | G | (4) |

Note (1) $T_a = 25 \pm 2$ °C

(2) Temperature and relative humidity range are shown in the figure below.

a. 90 % RH Max. ($T_a \leq 39$ °C)

b. Relative Humidity is 90% or less. ($T_a > 39$ °C)

c. No condensation

(3) 11ms, sine wave, one time for $\pm X$, $\pm Y$, $\pm Z$ axis

(4) 10-300 Hz, Sweep rate 10min, 30min for X,Y,Z axis

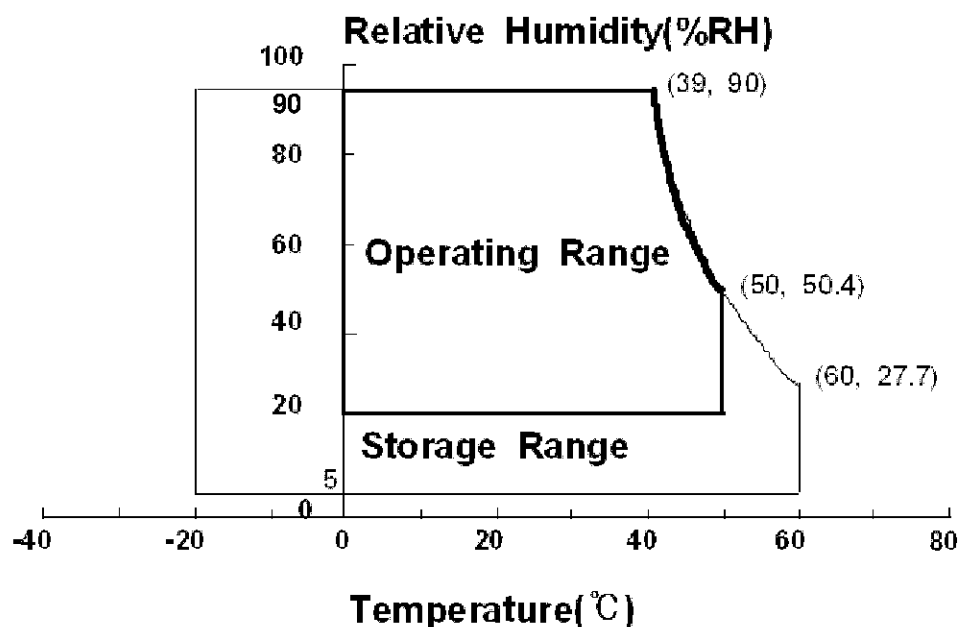
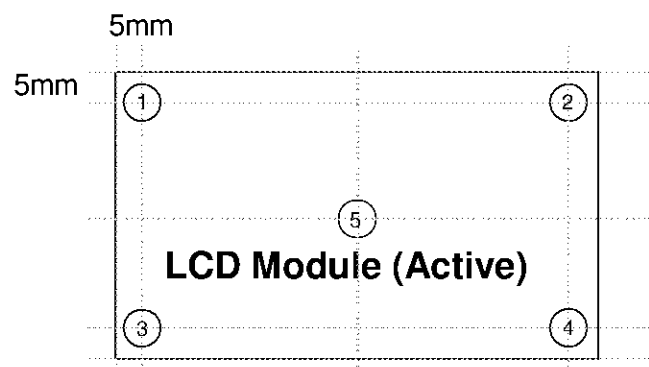


Fig. Temperature and Relative humidity range

(5) Definition of test point



ΔT should be less than $10\text{ }^{\circ}\text{C}$ ($\Delta T = |T_{\text{OPR}} - T_{\text{MAX}}|$)

T_{OPR} : Temperature of the center of the glass surface (Test point 5)

$T_1 \sim T_4$: Temperature of each edge of the glass surface

T_{MAX} : The highest temperature of the glass surface

2. Optical Characteristics

The optical characteristics should be measured in a dark room or equivalent.

Measuring equipment : TOPCON RD-80S, TOPCON SR-3 ,ELDIM EZ-Contrast

(Ta = 25 ± 2°C, VDD=12.0V, fv=60Hz, f_{DCLK}=148.5MHz, Max. Dimming =3.3V)

| Item | | Symbol | Condition | Min. | Typ. | Max. | Unit | Note |
|--|--------|---------------------|--|---------------|--------|---------------|-------------------|--------------------|
| Contrast Ratio (Center of screen) | | C/R | Normal $\theta_{\mathbf{L},\mathbf{R}}=0$ $\theta_{\mathbf{U},\mathbf{D}}=0$ Viewing Angle | - | 3,000 | - | | (1) SR-3 |
| Response Time | G-to-G | Tg | | - | 8 | - | msec | (3) RD-80S |
| Luminance of White (Center of screen) | | Y _L | | - | 500 | - | cd/m ² | (4) SR-3 |
| Color Chromaticity (CIE 1931) | Red | Rx | | TYP. -0.03 | 0.641 | TYP. +0.03 | | (5),(6) SR-3 |
| | | Ry | | | 0.336 | | | |
| | Green | Gx | | | 0.286 | | | |
| | | Gy | | | 0.610 | | | |
| | Blue | Bx | | | 0.146 | | | |
| | | By | | | 0.061 | | | |
| | White | Wx | | | 0.280 | | | |
| | | Wy | | | 0.290 | | | |
| Color Gamut | | - | | - | 72 | - | % | (5) SR-3 |
| Color Temperature | | - | | - | 10,000 | - | K | |
| Viewing Angle | Hor. | θ_{L} | C/R≥10 | 75 | 90 | - | Degree | (6) EZ-Contrast |
| | | θ_{R} | | 75 | 90 | - | | |
| | Ver. | θ_{U} | | 75 | 90 | - | | |
| | | θ_{D} | | 75 | 90 | - | | |
| Brightness Uniformity (9 Points) | | B _{uni} | | - | - | 25 | % | (2) SR-3 |

- Test Equipment Setup

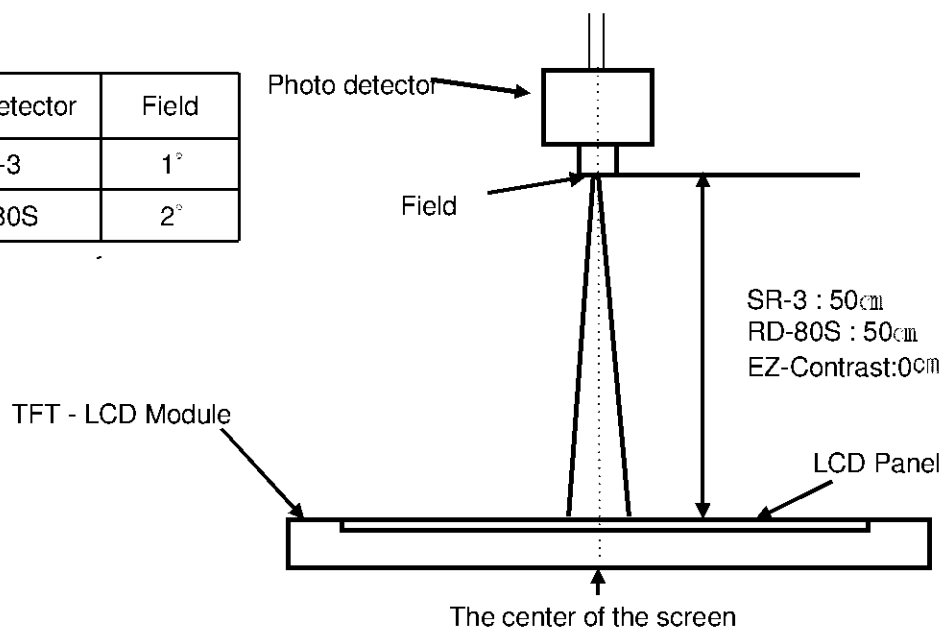
The measurement should be executed in a stable, windless and dark room between 40min and 60min after lighting the back light at the given temperature for stabilization of the back light. This should be measured in the center of screen.

. Max. Dimming = 3.3V

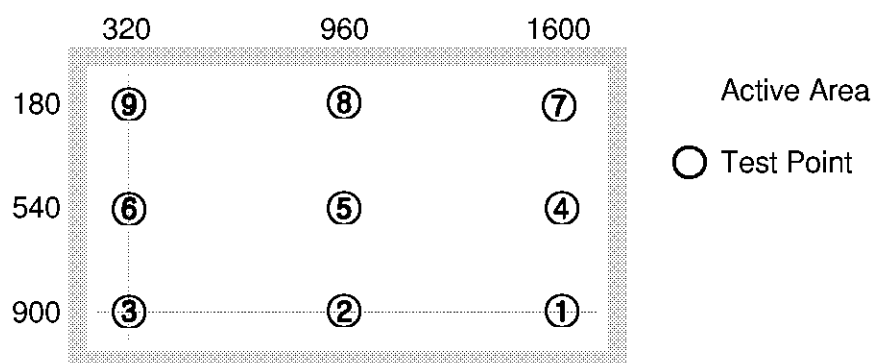
. Environment condition : Ta = 25 ± 2 °C

| | | | | | |
|-------|------------|---------|-----------------|------|--------|
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| Photo detector | Field |
|----------------|-------|
| SR-3 | 1° |
| RD-80S | 2° |



- Definition of test point



Note (1) Definition of Contrast Ratio (C/R)

: Ratio of gray max (Gmax) & gray min (Gmin) at the center point ⑤ of the panel

$$C/R = \frac{G_{\max}}{G_{\min}}$$

Gmax : Luminance with all pixels white

Gmin : Luminance with all pixels black

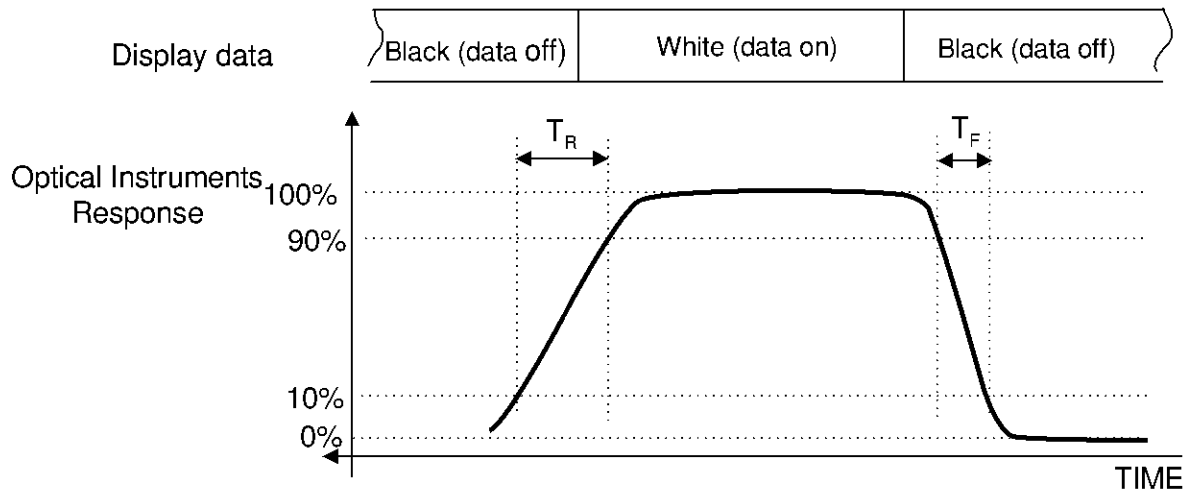
Note (2) Definition of 9 points brightness uniformity (Test pattern : Full White)

$$B_{uni} = 100 * \frac{(B_{max} - B_{min})}{B_{max}}$$

Bmax : Maximum brightness

Bmin : Minimum brightness

Note (3) Definition of Response time : Sum of Tr, Tf



※ G-to-G : Average response time between Gray to Gray (Scale)

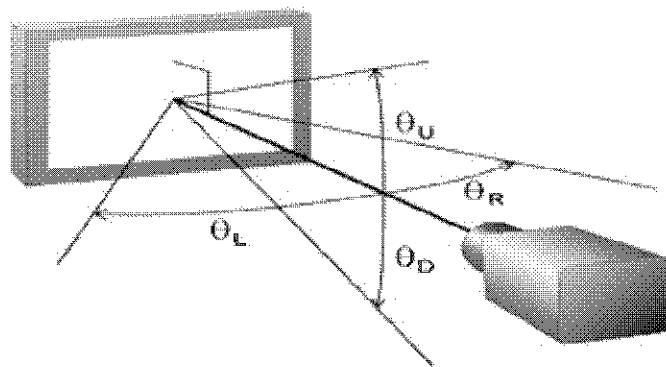
Note (4) Definition of Luminance of White : Luminance of white at center point ⑤

Note (5) Definition of Color Chromaticity (CIE 1931)

Color coordinate of Red, Green, Blue & White at center point ⑤

Note (6) Definition of Viewing Angle

: Viewing angle range ($C/R \geq 10$)



3. Electrical Characteristics

3.1 TFT LCD Module

The connector for display data & timing signal should be connected.

$T_a = 25^{\circ}\text{C} \pm 2^{\circ}\text{C}$

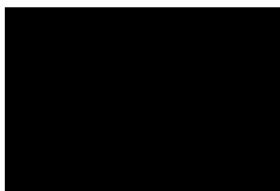
| Item | | Symbol | Min. | Typ. | Max. | Unit | Note |
|-------------------------|-----------------|-------------------|------|-------|------|------|---------|
| Voltage of Power Supply | | V _{DD} | 10.8 | 12 | 13.2 | V | (1) |
| Current of Power Supply | (a) Black | I _{DD} | - | 730 | - | mA | (2),(3) |
| | (b) White | | - | 1200 | - | mA | |
| | (c) Sub-Checker | | - | 1380 | - | mA | |
| Vsync Frequency | | f _V | - | 60 | - | Hz | |
| Hsync Frequency | | f _H | - | 67.5 | - | kHz | |
| Main Frequency | | Fdclk | - | 148.5 | - | MHz | |
| Rush Current | | I _{RUSH} | | - | 5 | A | (4) |

Note (1) The ripple voltage should be controlled under 10% of V_{DD} .

(2) $f_V=60\text{Hz}$, $f_{DCLK}=148.5\text{MHz}$, $V_{DD}=12.0\text{V}$, DC Current.

(3) Power dissipation check pattern (LCD Module only)

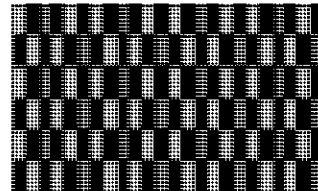
a) Black Pattern



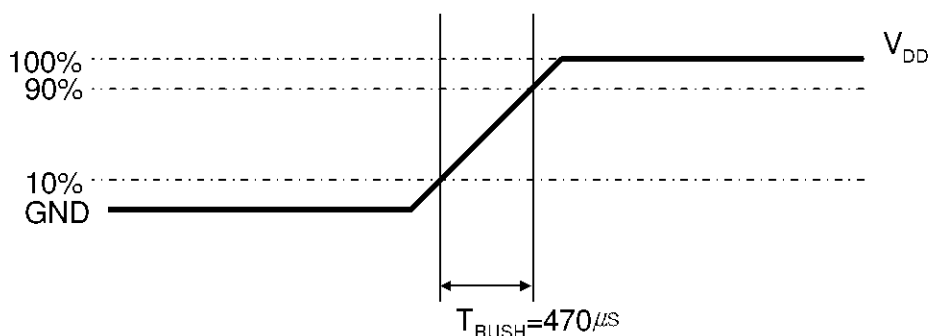
b) White Pattern



c) Sub-Checker



(4) Measurement Conditions

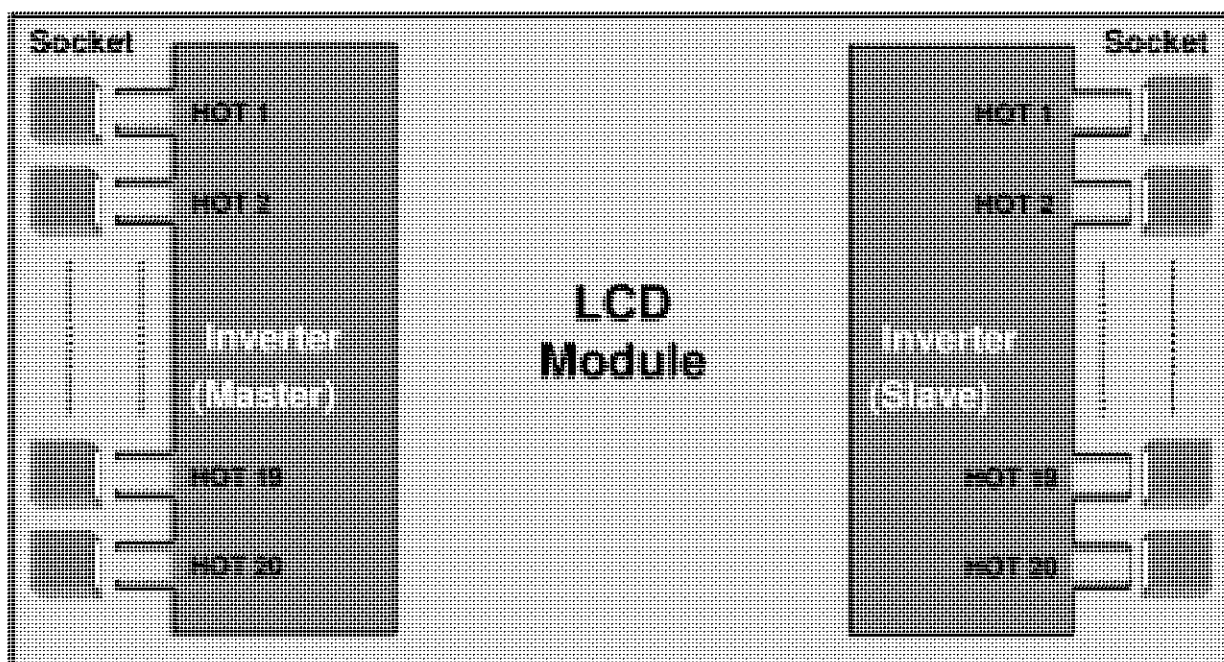


Rush Current I_{RUSH} can be measured when T_{RUSH} is $470 \mu\text{s}$.

3.2 Back Light Unit

The back light unit contains 20 direct-lighting type CCFLs (Cold Cathode Fluorescent Lamp). The characteristics of lamps are shown in the following tables.

$T_a = 25 \pm 2^\circ\text{C}$



| Item | Symbol | Min. | Typ. | Max. | Unit | Note |
|---------------------|--------|--------|------|------|------|------|
| Operating Life Time | Hr | 50,000 | - | - | Hour | (1) |

Note (1) It is defined as the time to take until the brightness reduces to 50% of its original value.

[Operating condition : $T_a = 25 \pm 2^\circ\text{C}$, $I_L = 9.0 \text{ mA}$, For single lamp only.]

| | | | | | |
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3.3 Inverter Input Condition & Specification

| Items | Symbol | Conditions | Specifications | | | Unit | Note |
|------------------|-------------------|--|----------------|------|------|-------|------------|
| | | | Min. | Typ. | Max. | | |
| Input Voltage | V _{in} | - | 22 | 24 | 26 | V | Ta=25±2 °C |
| Input Current | I _{RUSH} | V _{in} = 24V V _{dim} = 3.3V | - | - | 13.4 | A | (1) |
| | | | - | - | 11.2 | | (2) |
| Lamp Current | I _o | V _{in} = 24V V _{dim} = 3.3V | 8.5 | 9.0 | 9.5 | mArms | (2) |
| Frequency | F _{LAMP} | V _{in} = 24V | 40 | 42 | 44 | kHz | - |
| Backlight On/Off | ON | V _{in} = 24V | 2.4 | - | 5.5 | V | (3) |
| | OFF | V _{in} = 24V | 0 | - | 0.8 | | |
| Dimming Control | V _{DIM} | Max Lum | 3.3 | - | - | V | (4) |
| | | Min. Lum | - | - | 0 | | |

Note) Power Consumption is measured when 500[cd/m²] of luminance which is the typical luminance.

Lamp Current is measured at the point before Lamp.

(1) Max Value of the Power Consumption is measured during initial turn-on time* of the backlight.

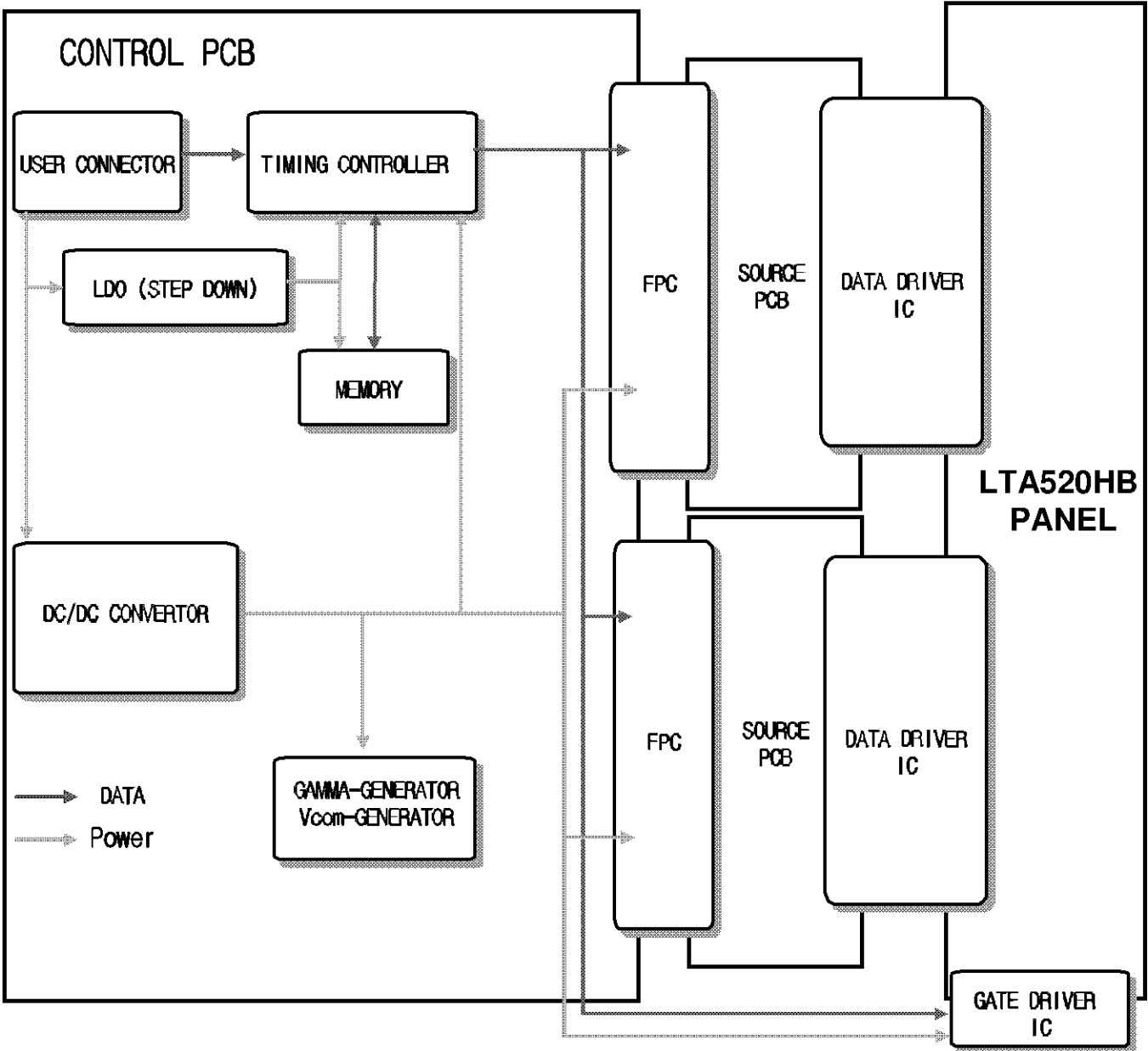
(2) Max Value of the Power Consumption is measured after 120 min warm-up.

(3) Inverter pin NO.12 is for backlight On/Off.

(4) Inverter pin NO.13 is for dimming control.

* Initial turn-on time : From 0sec to 60min after turn-on

4. Block Diagram



5. Input Terminal Pin Assignment

5.1. Input Signal & Power

Connector : FI-RE51S-HF (JAE)

| Pin | Symbol | Description | Pin | Symbol | Description |
|-----|--------|--------------------|-----|----------|--------------------|
| 1 | 12V | DC power supply | 26 | RE[0]P | Even LVDS Signal + |
| 2 | 12V | DC power supply | 27 | RE[1]N | Even LVDS Signal - |
| 3 | 12V | DC power supply | 28 | RE[1]P | Even LVDS Signal + |
| 4 | 12V | DC power supply | 29 | RE[2]N | Even LVDS Signal - |
| 5 | 12V | DC power supply | 30 | RE[2]P | Even LVDS Signal + |
| 6 | NC | NOTE1 | 31 | GND | Ground |
| 7 | GND | Ground | 32 | ROCLK- | Even LVDS Clock - |
| 8 | GND | Ground | 33 | ROCLK+ | Even LVDS Clock + |
| 9 | GND | Ground | 34 | GND | Ground |
| 10 | RO[0]N | Odd LVDS Signal - | 35 | RE[3]N | Even LVDS Signal - |
| 11 | RO[0]P | Odd LVDS Signal + | 36 | RE[3]P | Even LVDS Signal + |
| 12 | RO[1]N | Odd LVDS Signal - | 37 | NC | NOTE1 |
| 13 | RO[1]P | Odd LVDS Signal + | 38 | NC | |
| 14 | RO[2]N | Odd LVDS Signal - | 39 | GND | Ground |
| 15 | RO[2]P | Odd LVDS Signal + | 40 | NC | NOTE1 |
| 16 | GND | Ground | 41 | NC | |
| 17 | ROCLK- | Odd LVDS Clock - | 42 | NC | |
| 18 | ROCLK+ | Odd LVDS Clock + | 43 | NC | |
| 19 | GND | Ground | 44 | NC | |
| 20 | RO[3]N | Odd LVDS Signal - | 45 | LVDS_SEL | Ground |
| 21 | RO[3]P | Odd LVDS Signal + | 46 | NC | NOTE1 |
| 22 | NC | NOTE1 | 47 | NC | |
| 23 | NC | | 48 | NC | |
| 24 | GND | Ground | 49 | NC | |
| 25 | RE[0]N | Even LVDS Signal - | 50 | NC | |
| | | | 51 | NC | NOTE1 |

Note1) No Connection: These PINS are used only for SAMSUNG. (DO NOT CONNECT)

Note(1) Pin number starts from Left side

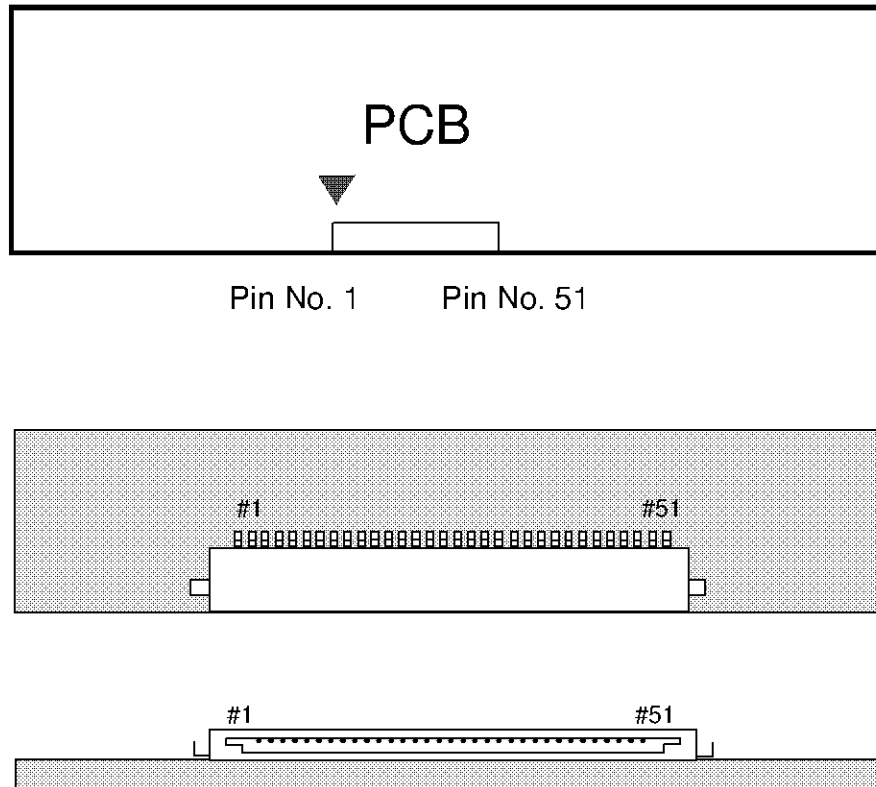


Fig. Connector diagram

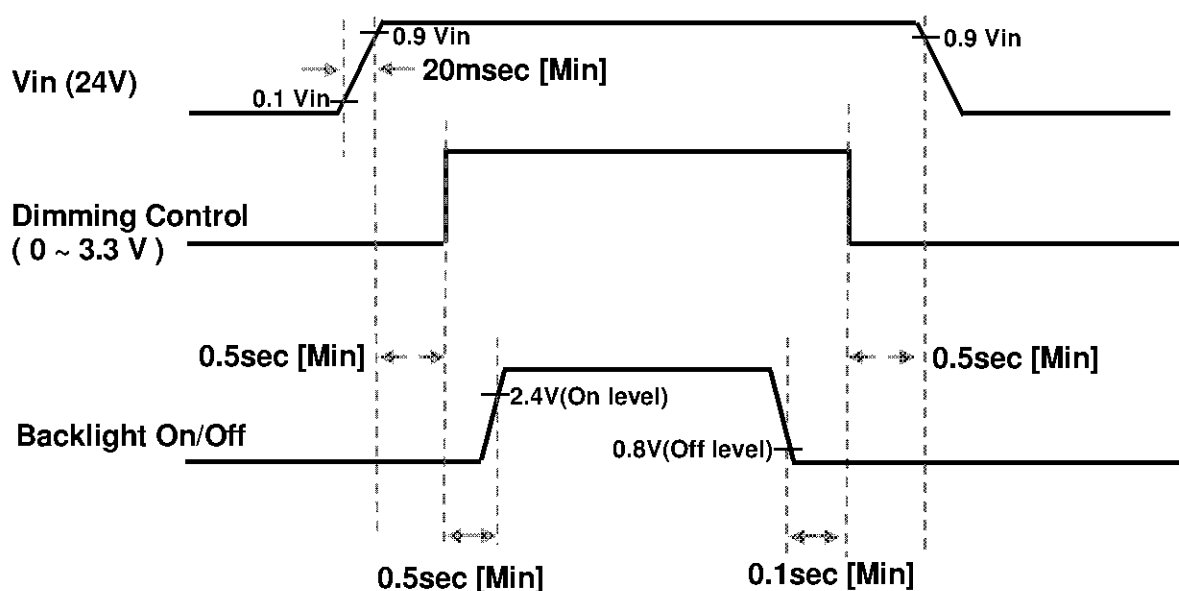
- a. Power GND pins should be connected to the LCD's metal chassis.
- b. All power input pins should be connected together.
- c. All NC pin should be separated from other signal or power.

5.2 Inverter Input Pin Configuration

Connector : JST, S14B-PHA-SM-TB(LF)

| Pin No. | Pin Configuration (FUNCTION) |
|---------|---|
| 1 | Vin (24 V) |
| 2 | Vin (24 V) |
| 3 | Vin (24 V) |
| 4 | Vin (24 V) |
| 5 | Vin (24 V) |
| 6 | GND |
| 7 | GND |
| 8 | GND |
| 9 | GND |
| 10 | GND |
| 11 | No Connection (DO NOT CONNECT) |
| 12 | Backlight On /Off [ON: 2.4 ~ 5.5 V, OFF: 0 ~ 0.8 V] |
| 13 | Dimming Control [0V: Min, 3.3V: Max] |
| 14 | No Connection (DO NOT CONNECT) |

5.3. Inverter Input Power Sequence



5.4 LVDS Interface

- LVDS Receiver : Tcon (merged)
- Data Format (JEIDA&VESA)

| | LVDS pin | JEIDA -DATA |
|-------------|--------------|-------------|
| TxOUT/RxIN0 | TxIN/RxOUT0 | R2 |
| | TxIN/RxOUT1 | R3 |
| | TxIN/RxOUT2 | R4 |
| | TxIN/RxOUT3 | R5 |
| | TxIN/RxOUT4 | R6 |
| | TxIN/RxOUT6 | R7 |
| | TxIN/RxOUT7 | G2 |
| TxOUT/RxIN1 | TxIN/RxOUT8 | G3 |
| | TxIN/RxOUT9 | G4 |
| | TxIN/RxOUT12 | G5 |
| | TxIN/RxOUT13 | G6 |
| | TxIN/RxOUT14 | G7 |
| | TxIN/RxOUT15 | B2 |
| | TxIN/RxOUT18 | B3 |
| TxOUT/RxIN2 | TxIN/RxOUT19 | B4 |
| | TxIN/RxOUT20 | B5 |
| | TxIN/RxOUT21 | B6 |
| | TxIN/RxOUT22 | B7 |
| | TxIN/RxOUT24 | HSYNC |
| | TxIN/RxOUT25 | VSNC |
| | TxIN/RxOUT26 | DEN |
| TxOUT/RxIN3 | TxIN/RxOUT27 | R0 |
| | TxIN/RxOUT5 | R1 |
| | TxIN/RxOUT10 | G0 |
| | TxIN/RxOUT11 | G1 |
| | TxIN/RxOUT16 | B0 |
| | TxIN/RxOUT17 | B1 |
| | TxIN/RxOUT23 | RESERVED |

5.5 Input Signals, Basic Display Colors and Gray Scale of Each Color

| COLOR | DISPLAY (8bit) | DATA SIGNAL | | | | | | | | | | | | | | | | | | | | | | | | GRAY SCALE LEVEL |
|------------------------------|-----------------------------|-------------|----|----|----|----|----|----|----|-------|----|----|----|----|----|----|----|------|----|----|----|----|----|----|------|------------------------|
| | | RED | | | | | | | | GREEN | | | | | | | | BLUE | | | | | | | | |
| | | R0 | R1 | R2 | R3 | R4 | R5 | R6 | R7 | G0 | G1 | G2 | G3 | G4 | G5 | G6 | G7 | B0 | B1 | B2 | B3 | B4 | B5 | B6 | B7 | |
| BASIC COLOR | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | BLUE | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| | GREEN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | CYAN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| | RED | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | MAGENTA | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| | YELLOW | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | - |
| | WHITE | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | - |
| GRAY SCALE OF RED | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R0 |
| | DARK ↑ ↓ LIGHT | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R1 |
| | | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R2 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | R3~ R252 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | |
| | | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R253 |
| | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R254 | |
| | RED | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | R255 |
| GRAY SCALE OF GREEN | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G0 |
| | DARK ↑ ↓ LIGHT | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G1 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G2 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | G3~ G252 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G253 |
| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G254 | |
| | GREEN | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | G255 |
| GRAY SCALE OF BLUE | BLACK | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | B0 |
| | DARK ↑ ↓ LIGHT | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | B1 |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | B2 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | B3~ B252 |
| | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | : | : | : | : | : | : | | | |
| | | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | B253 |
| | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | B254 | |
| | BLUE | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | 1 | B255 |

Note) Definition of Gray :

Rn : Red Gray, Gn : Green Gray, Bn : Blue Gray (n = Gray level)

Input Signal : 0 = Low level voltage, 1 = High level voltage

6. Interface Timing

6.1 Timing Parameters (DE only mode)

| SIGNAL | ITEM | SYMBOL | MIN. | TYP. | MAX. | Unit | NOTE |
|-------------------------|-----------------------|----------|------|-------|------|--------|------|
| Clock | Frequency | $1/T_C$ | - | 148.5 | - | MHz | - |
| Hsync | | F_H | - | 67.5 | - | KHz | - |
| Vsync | | F_V | - | 60 | - | Hz | - |
| Vertical Display Term | Active Display Period | T_{VD} | - | 1080 | - | Lines | - |
| | Vertical Total | T_V | - | 1125 | - | Lines | - |
| Horizontal Display Term | Active Display Period | T_{HD} | - | 1920 | - | Clocks | - |
| | Horizontal Total | T_H | - | 2200 | - | clocks | - |

Note) This product is DE only mode. The input of Hsync & Vsync signal does not have an effect on normal operation.

(1) Test Point : TTL control signal and CLK at LVDS Tx input terminal in system

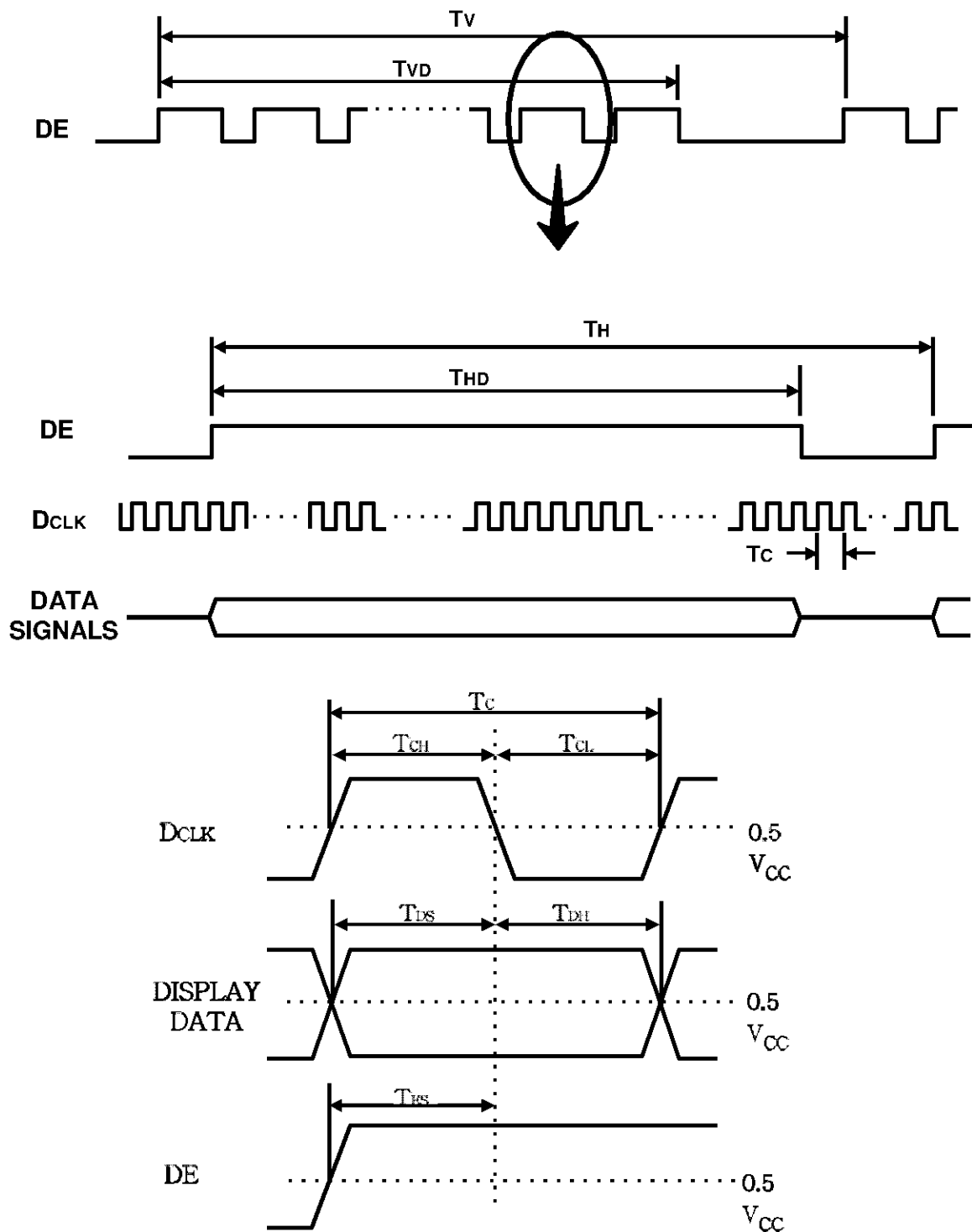
(2) Internal $V_{DD} = 3.3V$

(3) Spread spectrum

- Modulation rate (max) : $\pm 1.5 \%$

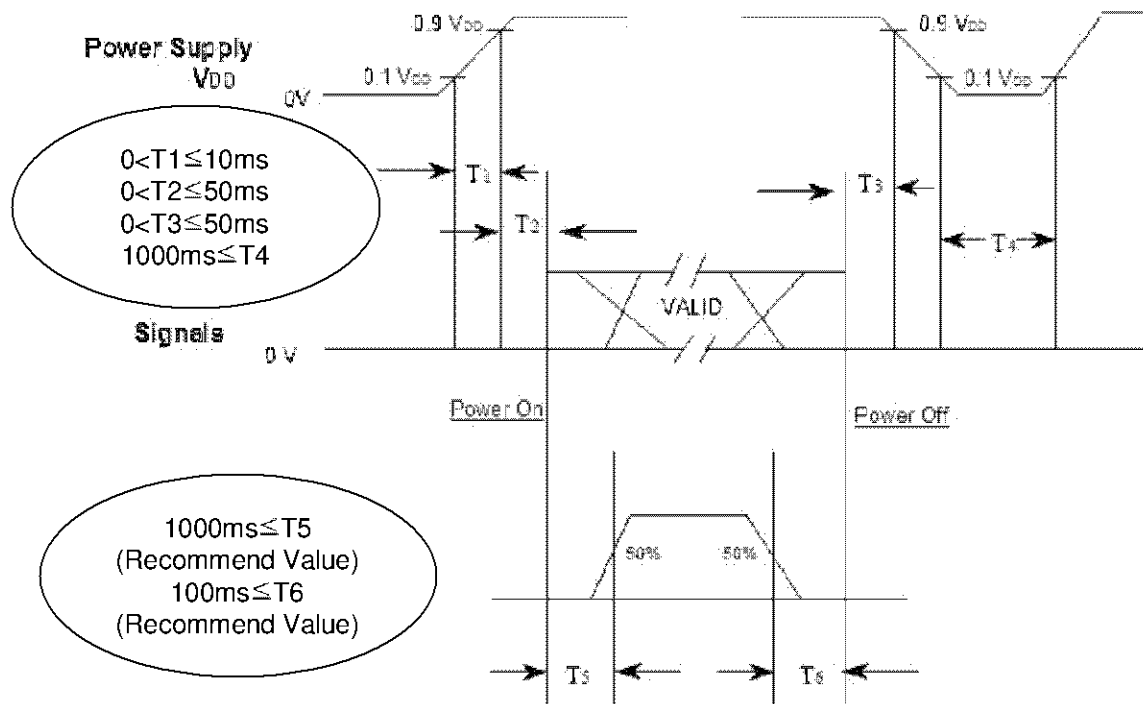
- Modulation Frequency : under 100KHz

6.2 Timing diagrams of interface signal (DE only mode)



6.3 Power ON/OFF Sequence

To prevent a latch-up or DC operation of the LCD Module, the power on/off sequence should be as the diagram below.



T₁ : V_{DD} rising time from 10% to 90%

T₂ : The time from V_{DD} to valid data at power ON.

T₃ : The time from valid data off to V_{DD} off at power Off.

T₄ : V_{DD} off time for Windows restart

T₅ : The time from valid data to B/L enable at power ON.

T₆ : The time from valid data off to B/L disable at power Off.

- The supply voltage of the external system for the Module input should be the same as the definition of V_{DD}.
- Apply the lamp voltage within the LCD operation range. When the back light turns on before the LCD operation or the LCD turns off before the back light turns off, the display may momentarily show abnormal screen.
- In case of V_{DD} = off level, please keep the level of input signals low or keep a high impedance.
- T₄ should be measured after the Module has been fully discharged between power off and on period.
- Interface signal should not be kept at high impedance when the power is on.

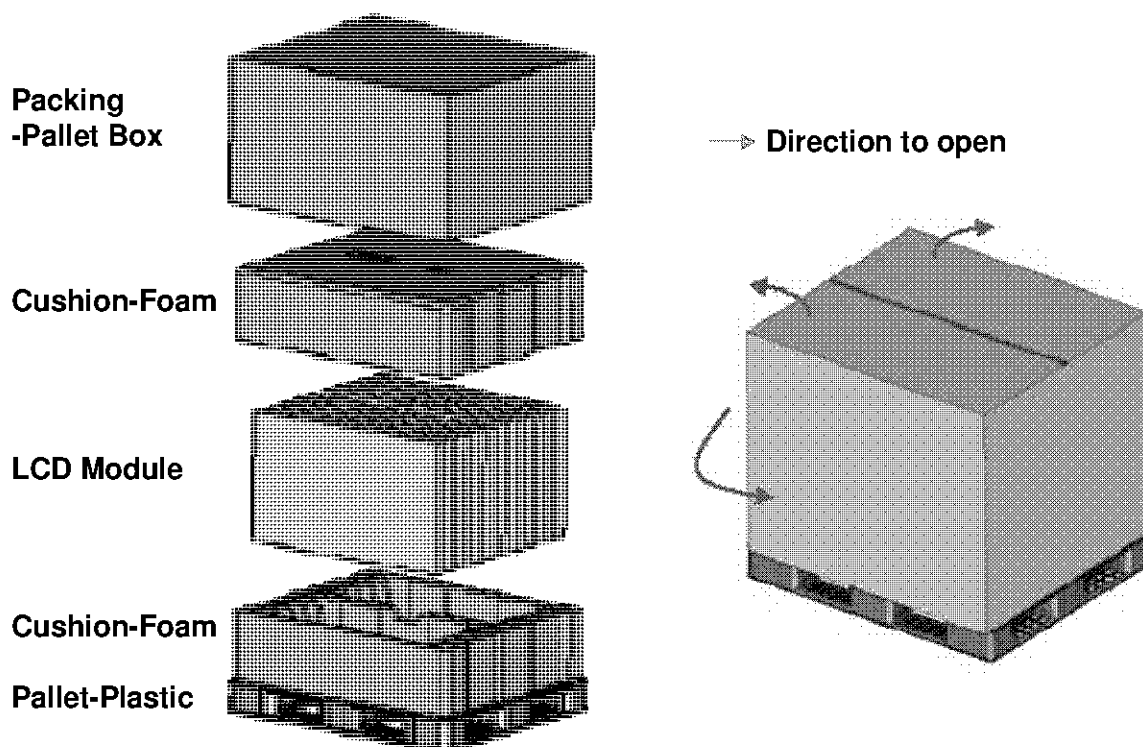
8. PACKING

8.1 CARTON (Internal Package)

(1) Packing Form

Corrugated fiberboard box and corrugated cardboard as shock absorber

(2) Packing Method



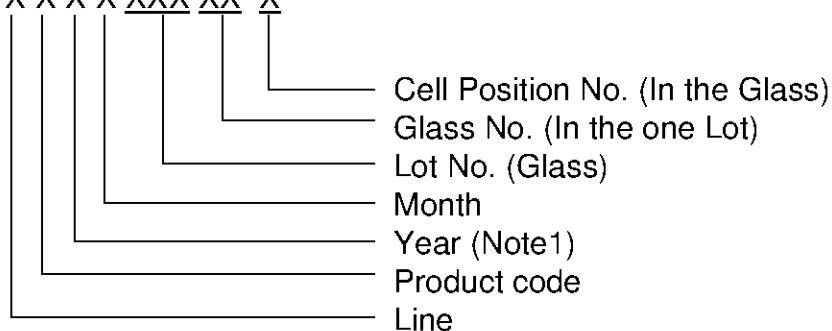
8.2 Packing Specification

| Item | Specification | Remark |
|---------------------|-----------------------------|---|
| LCD Packing | 10ea / (Packing-Pallet Box) | 1. 180 Kg / LCD (10ea) 2. 14.0 Kg / Cushion-pallet (2ea) 3. 8.0 Kg / Packing-Pallet Box (1ea) 4. Cushion-pallet Material : EPS 5. Packing-Pallet Box Material : SW3 |
| Pallet | 1Box / Pallet | 1. Pallet weight = 10kg |
| Packing Direction | Vertical | |
| Total Pallet Size | H x V x height | 1475mm(H) x 1150mm(V) x 935mm(height) |
| Total Pallet Weight | 212.0 kg | Pallet(10kg) + Module(180kg) + Cushion(up+bottom=14.0kg) + Pallet-BOX(8.0kg) |

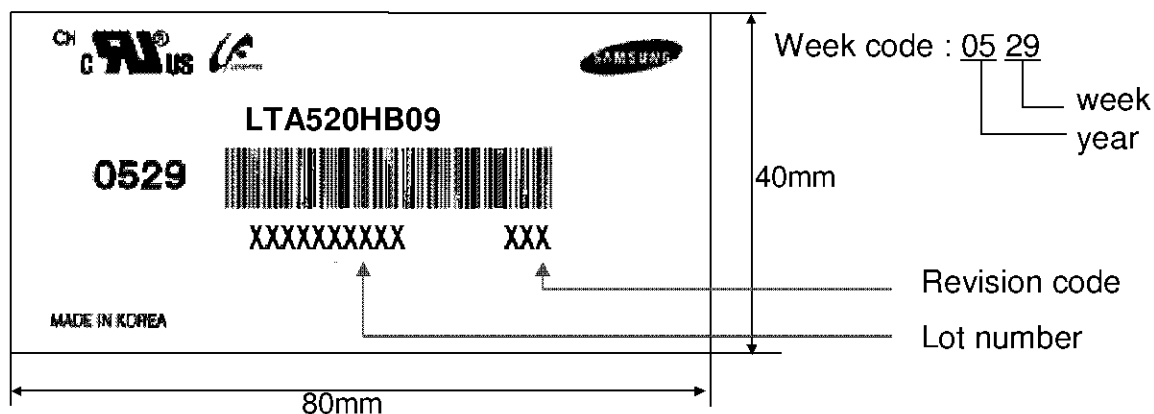
9. MARKING & OTHERS

A nameplate bearing followed by is affixed to a shipped product at the specified location on each product.

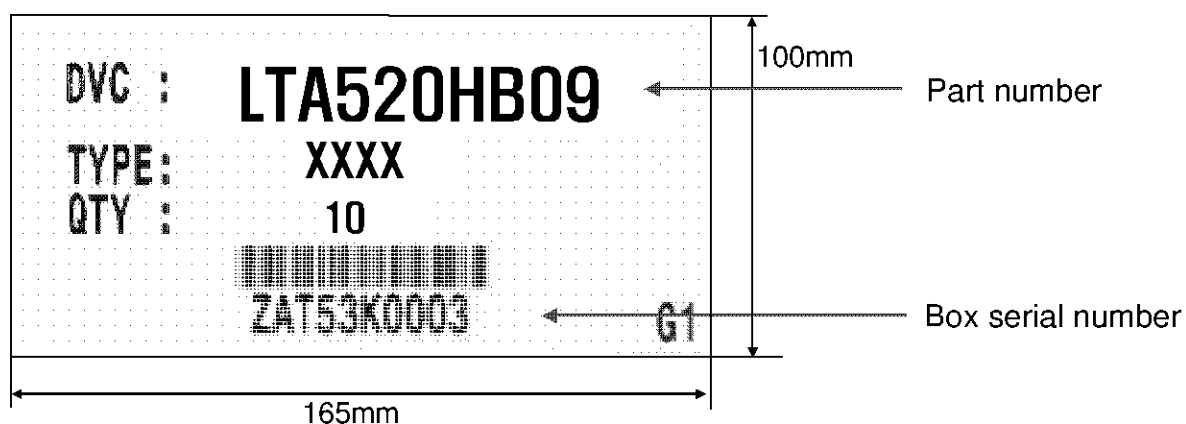
- (1) Part number : LTA520HB09
- (2) Revision: Three letters
- (3) Lot number : X X X X XXX XX X



(4) Nameplate Indication



(5) Packing box attach



(6) Others

- 1. After service part

Lamps cannot be replaced because of the narrow bezel structure.

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10. General Precautions

10.1 Handling

- (a) When the Module is assembled, it should be attached to the system firmly using all mounting holes. Be careful not to twist and bend the Module.
- (b) Because the inverter use high voltage, it should be disconnected from power before it is assembled or disassembled.
- (c) Refrain from strong mechanical shock and / or any force to the Module.
In addition to damage, this may cause improper operation or damage to the Module and CCFL back light.
- (d) Note that polarizers are very fragile and could be damage easily.
Do not press or scratch the surface harder than a HB pencil lead.
- (e) Wipe off water droplets or oil immediately. If you leave the droplets for a long time, staining or discoloration may occur.
- (f) If the surface of the polarizer is dirty, clean it using absorbent cotton or soft cloth.
- (g) Desirable cleaners are water, IPA(Isopropyl Alcohol) or Hexane.
Do not use Ketone type materials(ex. Acetone), Ethyl alcohol, Toluene, Ethyl acid or Methyl chloride. It might permanent damage to the polarizer due to chemical reaction.
- (h) If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth . In case of contact with hands, legs or clothes, it must be washed away with soap thoroughly.
- (i) Protect the module from Electrostatic discharge. Otherwise the ASIC IC or Semiconductor would be damaged.
- (j) Use finger-stalls with soft gloves in order to keep display clean during the incoming inspection and assembly process.
- (k) Do not disassemble the Module.
- (l) Do not disassemble shield case of inverter & LVDS board.
- (m) Do not connect N.C pins. (Samsung internal use only)
- (n) Protection film for polarizer on the Module should be slowly peeled off just before use so that the electrostatic charge can be minimized. Must put on antistatic glove while handle a module
- (o) Pins of I/F connector should not be touched directly with bare hands.

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10.2 Storage

- (a) Do not leave the Module in high temperature, and high humidity for a long time. It is highly recommended to store the Module with temperature from 0 to 35 °C and relative humidity of less than 70%.
- (b) Do not store the TFT-LCD Module in direct sunlight.
- (c) The Module should be stored in a dark place. It is prohibited to apply sunlight or fluorescent light in storing.

10.3 Operation

- (a) Do not connect or disconnect the Module in the "Power On" condition.
- (b) Power supply should always be turned on/off by the "Power on/off sequence"
- (c) Module has high frequency circuits. Sufficient suppression to the electromagnetic interference should be done by system manufacturers. Grounding and shielding methods may be important to minimize the interference.
- (d) The cable between the back light connector and its inverter power supply should be connected directly with a minimized length. A longer cable between the back light and the inverter may cause lower luminance of lamp(CCFT) and may require higher startup voltage(Vs).

10.4 Operation Condition Guide

- (a) The LCD product should be operated under normal conditions.
Normal condition is defined as below;
 - Temperature : $20 \pm 15^{\circ}\text{C}$
 - Humidity : $55 \pm 20\%$
 - Display pattern : continually changing pattern (Not stationary)
- (b) If the product will be used in extreme conditions such as high temperature, humidity, display patterns or operation time etc., It is strongly recommended to contact SEC for Application engineering advice. Otherwise, its reliability and function may not be guaranteed. Extreme conditions are commonly found at Airports, Transit Stations, Banks, Stock market, and Controlling systems.

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10.5 Others

- (a) Ultra-violet ray filter is necessary for outdoor operation.
- (b) Avoid condensation of water. It may result in improper operation or disconnection of electrode.
- (c) Do not exceed the absolute maximum rating value. (supply voltage variation, input voltage variation, variation in part contents and environmental temperature, and so on)
Otherwise the Module may be damaged.
- (d) If the Module keeps displaying the same pattern for a long period of time, the image may be "sticked " to the screen.
To avoid image sticking, it is recommended to use a screen saver.
- (e) This Module has its circuitry PCB's on the rear side and should be handled carefully in order not to be stressed.
- (f) Please contact SEC in advance when you display the same pattern for a long time.

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